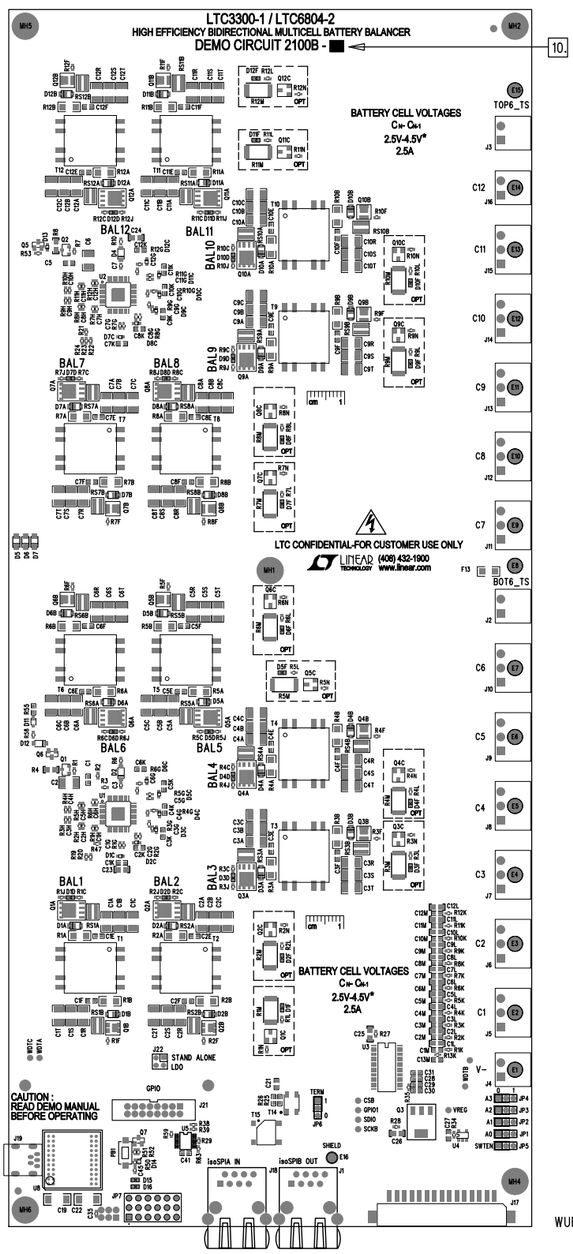
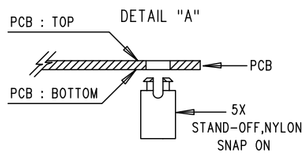
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
–	1	PRODUCTION	JIM D.	02-25-15

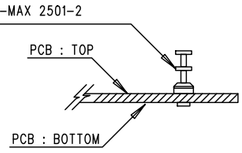


NOTES: UNLESS OTHERWISE SPECIFIED

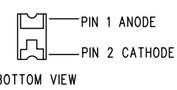
1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
   MAXIMUM SOLDER TEMPERATURE IS <250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
   LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
   MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. APPLY ASSEMBLY STAMP OR QA STAMP TO BOTTOM OF BOARD (UNSHOWY AREA).
7. INSTALL TURRETS, STAND-OFFS AND BANANA JACKS AS SHOWN BELOW:



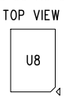
(5 PLCS) : MILL-MAX 2308
(18 PLCS) : MILL-MAX 2501-2



8. INSTALL LED'S D1D-D12D, D1F-D12F, D15 AND D16 AS SHOWN:



9. INSTALL U8 AS SHOWN BELOW:

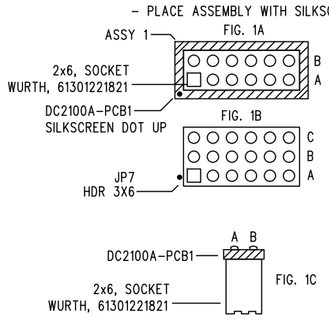


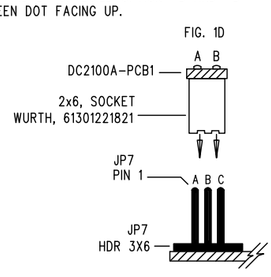
NOTE: FOR ASSEMBLY AND REFLOW RECOMENDATIONS
PLEASE USE LINK PROVIDED BELOW:
http://cds.linear.com/docs/en/product-info/L2_LGA-assy_02-06-2015.pdf

10. THERE ARE 4 ASSEMBLY VERSIONS FOR THIS DESIGN: -A AND -B, -C AND -D. PLEASE SEE BILL OF MATERIALS TO MARK EACH VERSION CORRECTLY BY BLACK PERMANENT MARKER.

11. INSTALLATION OF DC2100A-ASSY 1 FOR DC2100B-A, -B, -C AND -D, IS AS FOLLOWS: REFER TO FIG. 1A, 1B, 1C AND 1D.

- PLUG ASSY 1 INTO JP7 ROWS B AND C FOR BUILD OPTION -A AND -C
- PLUG ASSY 1 INTO JP7 ROWS A AND B FOR BUILD OPTION -B AND -D
- PLACE ASSEMBLY WITH SILKSCREEN DOT FACING UP.





11. PLEASE NOTE: DO NOT INSERT DC2100A THERMISTOR BOARDS TO DC2100 DEMO BOARD PRIOR TO SHIPMENT. AFTER FINAL ASSEMBLY IS COMPLETED OF THE DC2100A THERMISTOR BOARDS THEY NEED TO BE PLACED IN A SMALL BAG AS LISTED ON BOM AND PLACED IN THE BAG OF THE DC2100B DEMO BOARD.

APPROVALS	
PCB DES.	NC
APP ENG.	JIM D.

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TITLE: TOP ASSEMBLY DRAWING:
HIGH EFFICIENCY BIDIRECTIONAL MULTICELL BATTERY BALANCER

SIZE	IC NO.	REV.
N/A	LTC3300ILXE-1/LTC6804IG-2 DEMO CIRCUIT 2100B-A/D	1
SCALE = NONE	FILENAME: DC2100B-1PCB	SHT 1 of 2

D